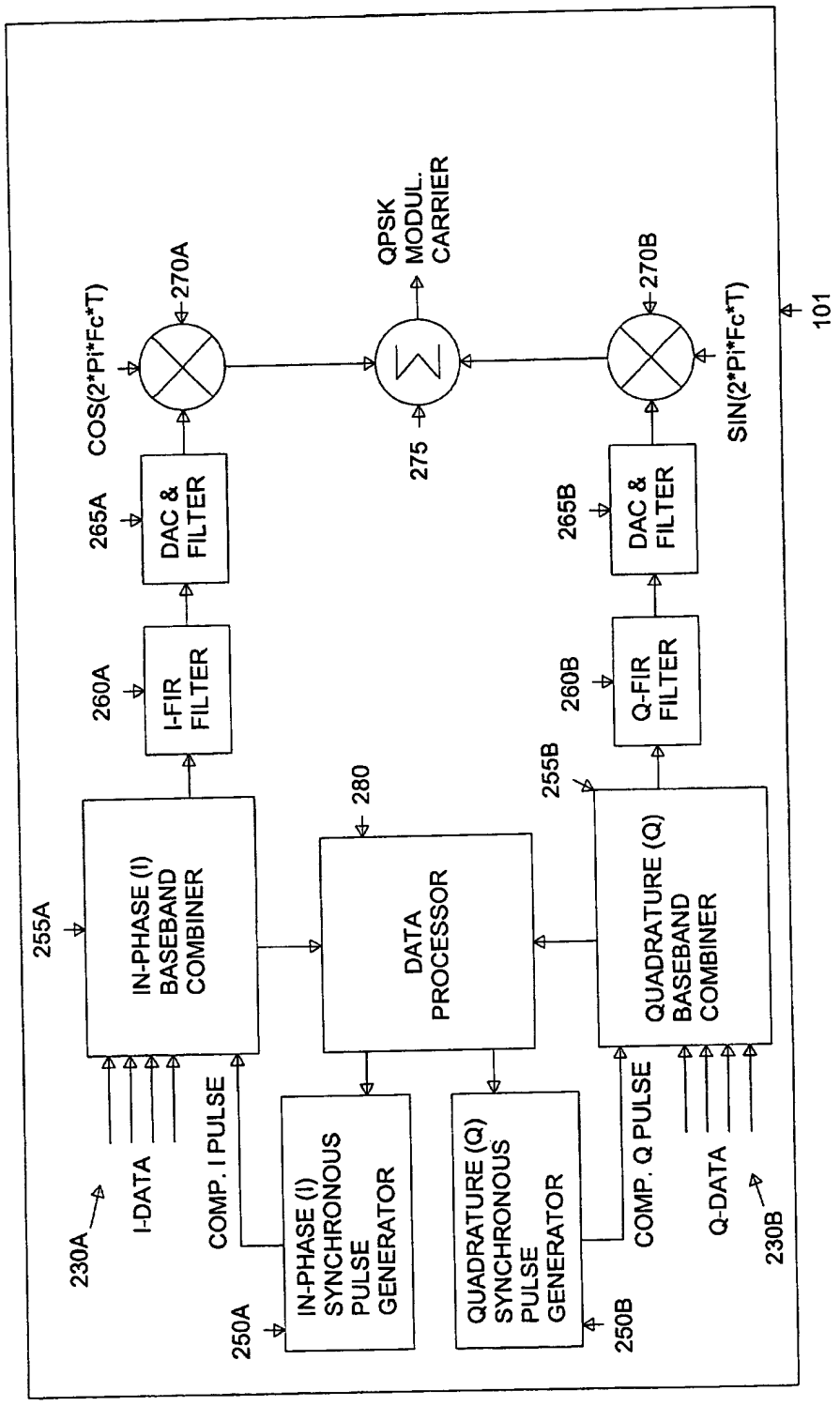
101
QPSK MODUL. CARRIER
275
270A
270B
COS(2*Pi*Fc*T)
SIN(2*Pi*Fc*T)
265A
265B
DAC & FILTER
DAC & FILTER
260A
260B
I-FIR FILTER
Q-FIR FILTER
255A
255B
280
IN-PHASE (I) BASEBAND COMBINER
DATA PROCESSOR
QUADRATURE (Q) BASEBAND COMBINER
230A
I-DATA
COMP. I PULSE
250A
IN-PHASE (I) SYNCHRONOUS PULSE GENERATOR
QUADRATURE (Q) SYNCHRONOUS PULSE GENERATOR
250B
COMP. Q PULSE
Q-DATA
230B

FIGURE 2B

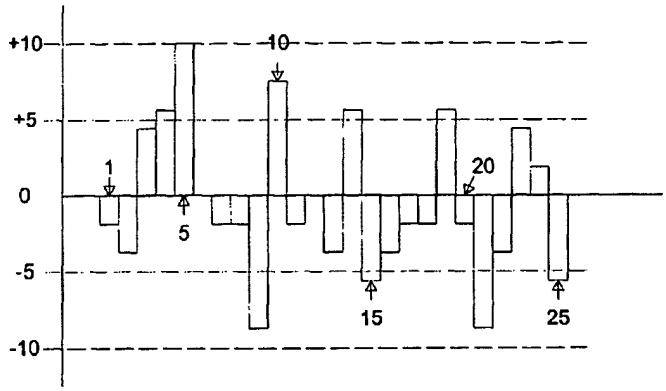

FIGURE 3A

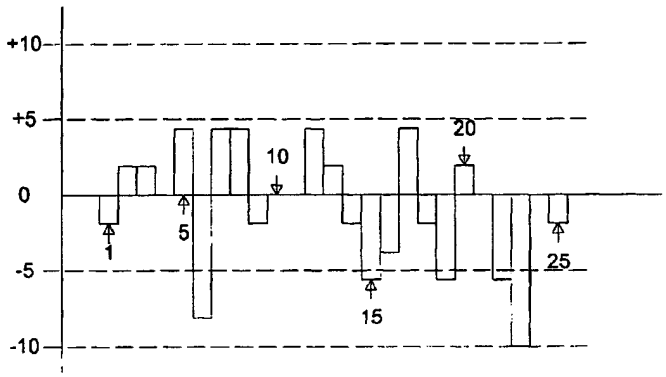

FIGURE 3B

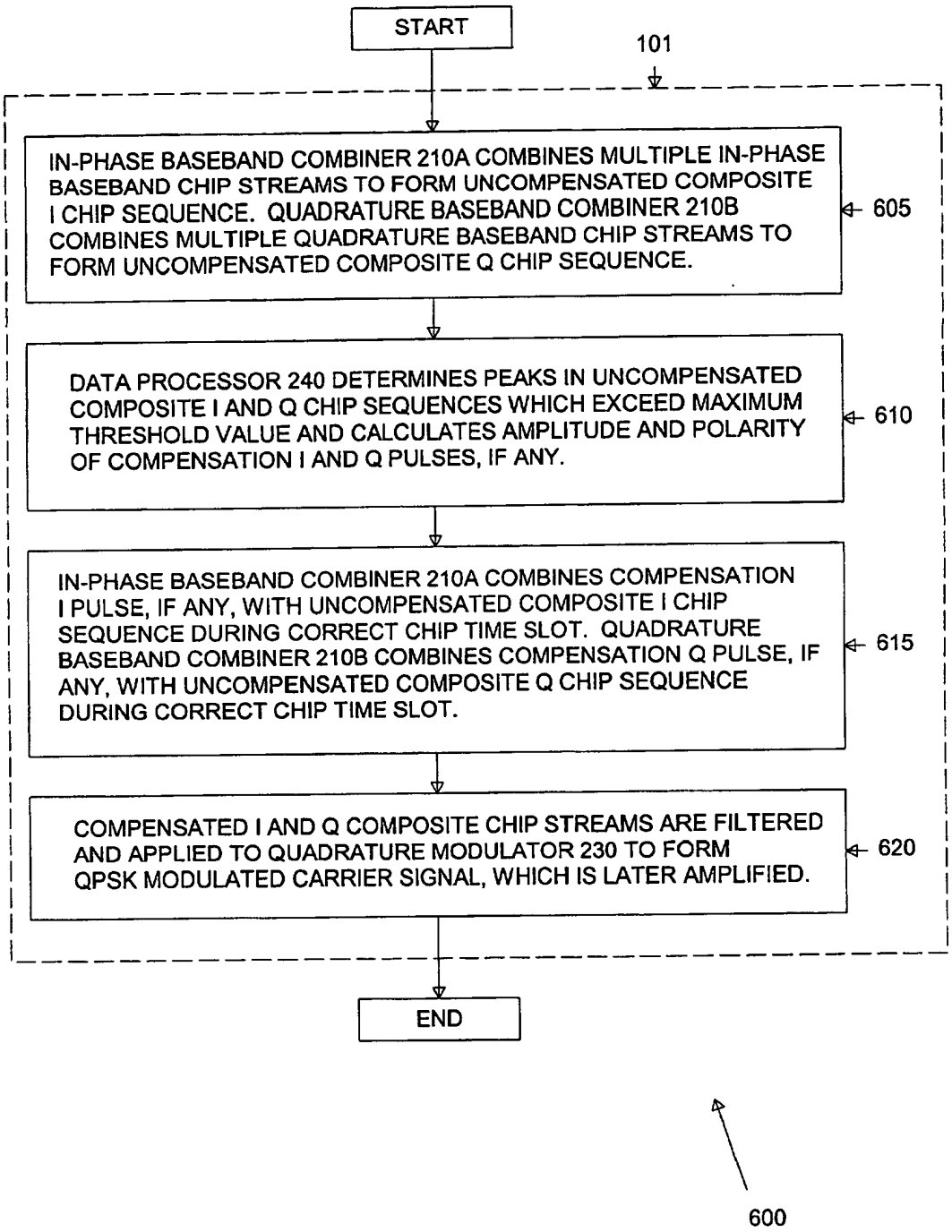
START
101
IN-PHASE BASEBAND COMBINER 210A COMBINES MULTIPLE IN-PHASE BASEBAND CHIP STREAMS TO FORM UNCOMPENSATED COMPOSITE I CHIP SEQUENCE. QUADRATURE BASEBAND COMBINER 210B COMBINES MULTIPLE QUADRATURE BASEBAND CHIP STREAMS TO FORM UNCOMPENSATED COMPOSITE Q CHIP SEQUENCE.
605
DATA PROCESSOR 240 DETERMINES PEAKS IN UNCOMPENSATED COMPOSITE I AND Q CHIP SEQUENCES WHICH EXCEED MAXIMUM THRESHOLD VALUE AND CALCULATES AMPLITUDE AND POLARITY OF COMPENSATION I AND Q PULSES, IF ANY.
610
IN-PHASE BASEBAND COMBINER 210A COMBINES COMPENSATION I PULSE, IF ANY, WITH UNCOMPENSATED COMPOSITE I CHIP SEQUENCE DURING CORRECT CHIP TIME SLOT. QUADRATURE BASEBAND COMBINER 210B COMBINES COMPENSATION Q PULSE, IF ANY, WITH UNCOMPENSATED COMPOSITE Q CHIP SEQUENCE DURING CORRECT CHIP TIME SLOT.
615
COMPENSATED I AND Q COMPOSITE CHIP STREAMS ARE FILTERED AND APPLIED TO QUADRATURE MODULATOR 230 TO FORM QPSK MODULATED CARRIER SIGNAL, WHICH IS LATER AMPLIFIED.
620
END
600

FIGURE 6